



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/713,572
Filing Date: November 12, 2003
Applicant: Kazuaki SAKURADA
Group Art Unit: 1762
Examiner: Brian K. Talbot
Title: MANUFACTURING METHOD OF MULTILAYER
CIRCUIT BOARD
Attorney Docket: 9319G-000593

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT

Sir:

In response to the Final Office Action mailed August 10, 2007, and in conjunction with Applicant's Request for Continued Examination, please amend the application as follows and consider the remarks set forth below.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 5 of this paper.